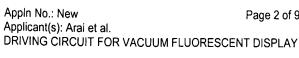
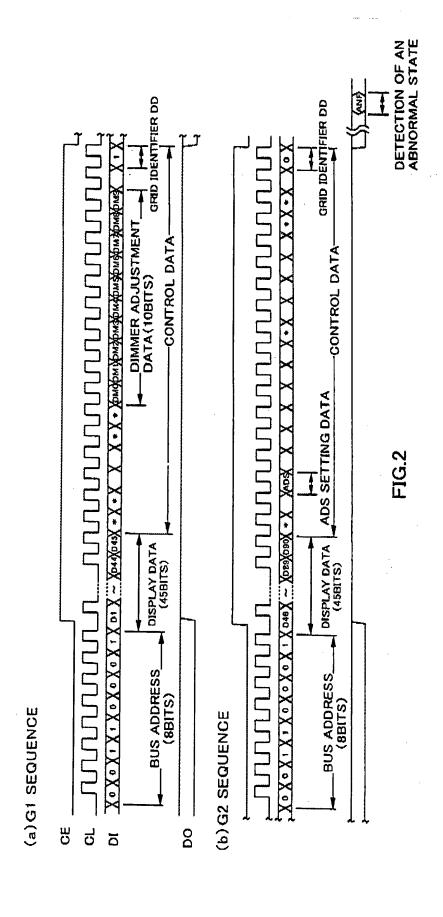
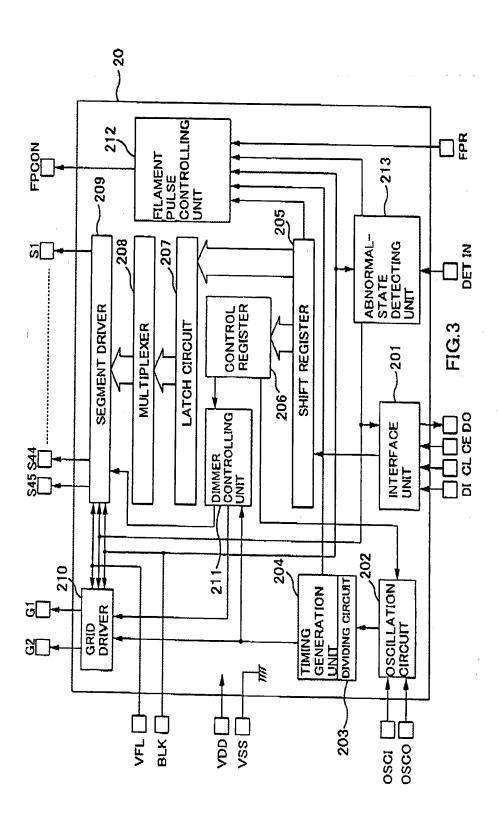


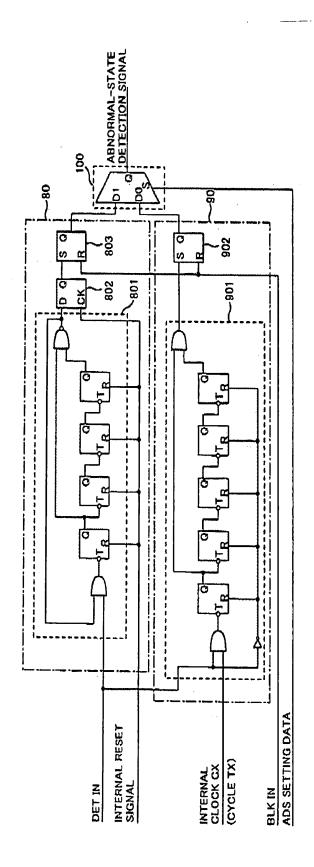
FIG.1

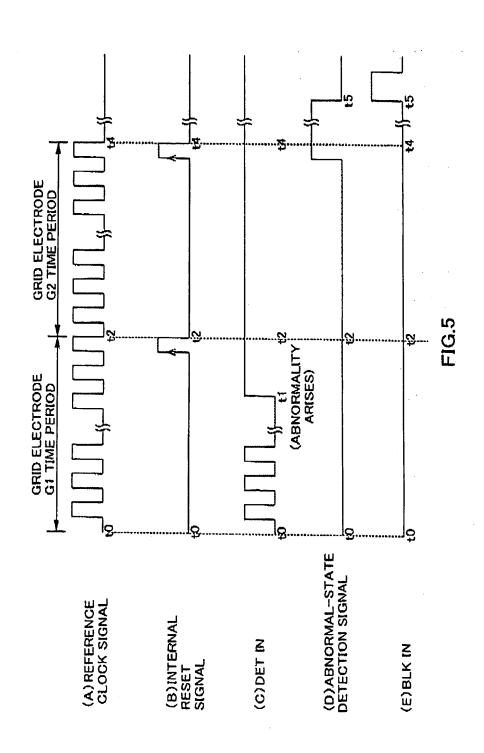




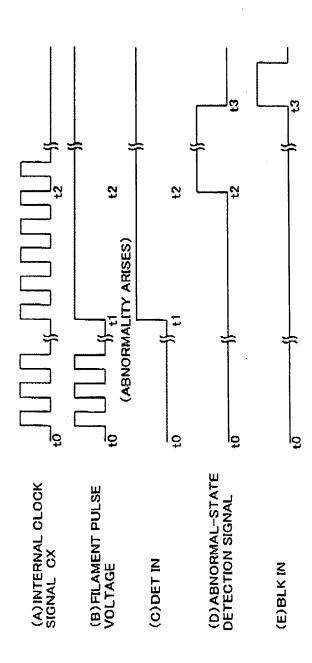


Appln No.: New Page 4 of 9
Applicant(s): Arai et al.
DRIVING CIRCUIT FOR VACUUM FLUORESCENT DISPLAY





Appln No.: New Page 6 of 9
Applicant(s): Arai et al.
DRIVING CIRCUIT FOR VACUUM FLUORESCENT DISPLAY



Appln No.: New Page 7 of 9
Applicant(s): Arai et al.
DRIVING CIRCUIT FOR VACUUM FLUORESCENT DISPLAY

